## ECE 320 - Homework \#9

TTL Logic, MOSFET Theory, MOSFET Switch. Due Monday, October 23rd, 2017

## TTL Logic

1) For the following TTL inverter

1a) Determine the voltages and currents


1b) Determine how many 36 uA loads you can connect to Y and keep the voltage at $\mathrm{Y}>4 \mathrm{~V}$ (the fan-out high) For the voltage to drop 1 V , you need 1 mA through the 1 k resistor. The number of loads is then

$$
n=\frac{1 m A}{36 \mu A}=27.78
$$

You can drive 27 loads (fan-out high = 27)
note: From problem \#2, it should have been 28.8uA. This results in a fanout of

$$
n=\frac{1 m A}{28.8 \mu A}=34.72
$$

You can actually drive 34 loads (fan-out high = 34)
2) For the following TTL inverter

2a) Determine the voltages and currents


2b) Determine how many 50k loads you can attach to the load and keep the trnansistor saturated (the fanout low)
For the second transistor to be saturated:

$$
\beta I_{b}>I_{c}
$$

$$
10.08 \mathrm{~mA}>I_{c}
$$

Each load adds 96uA

$$
\begin{aligned}
& 4.8 m A+n \cdot 96 \mu A=10.8 m A=I_{c} \\
& n=62.5
\end{aligned}
$$

This circuit can sink current from 62 loads while keeping the second transistor saturated.

The fan-out low is 62 .

## MOSFET Theory

3) For the MOSFET graph given on the back of this page,

- Determine the transconductance gain, kn,
- Mark the off / saturated / ohmic regions

Pick a point in the saturated region

- $\mathrm{Vgs}=10 \mathrm{~V}$
- V th $=2 \mathrm{~V}$
- $V d s=10 \mathrm{~V}$
- Ids = 7.5A

Plug this into the transisor equation

$$
\begin{aligned}
& I_{d s}=\frac{K_{n}}{2}\left(V_{g s}-V_{t h}\right)^{2} \\
& 7.5 A=\frac{K_{n}}{2}(10-2)^{2} \\
& K_{n}=0.2344 \frac{A}{V^{2}}
\end{aligned}
$$


4) On this graph, draw the load-line for the following circuit. Also mark the operating point when

| $\mathrm{Vg}=0 \mathrm{~V}$ | Off | $\mathrm{Vds}=10 \mathrm{~V}$ | $\mathrm{Ids}=0$ |
| :--- | :--- | :--- | :--- |
| $\mathrm{Vg}=5 \mathrm{~V}$ | Saturated | $\mathrm{Vds}=6.25 \mathrm{~V}$ | Ids $=1875 \mathrm{~mA}$ |
| $\mathrm{Vg}=10 \mathrm{~V}$ | Ohmic | $\mathrm{Vds}=2.38 \mathrm{~V}$ | Ids $=3810 \mathrm{~mA}$ |


5) A MOSFET has the following characteristics

- Rds = 0.65 Ohms @ 4A when Vgs = 10V.
- $\quad \mathrm{Vth}=2.0 \mathrm{~V}$

Design a switch which allows this MOSFET to turn on and off an 8 Ohm speaker at 10 V using a $0 \mathrm{~V} / 10 \mathrm{~V}$ souce.

When $\mathrm{Vg}=0 \mathrm{~V}$
Vgs $=0 \mathrm{~V}<\mathrm{Vth}$
MOSFET is off
Id $=0$

When $\mathrm{Vg}=10 \mathrm{~V}$
Rds $=0.65$ Ohms (approx)
$I_{d s}=\frac{10 \mathrm{~V}}{0.65 \Omega+8 \Omega}=1.156 \mathrm{~A}$
$V_{d s}=0.65 \Omega \cdot 1.156 A=0.75 \mathrm{~V}$


## Lab: Term Project (part 2)

Design one part of your term project. Some suggestions are:

- Use a Schmitt Trigger (part 1) and an AC to DC converter (part 2) to drive a 12 V DC motor when the temperature is below 5C.
- Use a DTL NAND gate (part 1) and an H-bridge (part 2) to drive a 10V DC motor forward when switch when $\overline{A B}$ is true, reverse when false
- Use an AC to DC converter (part 1) to convert 20 Vp 60 Hz AC to 20VDC, capable of 100 mA (part 1), which then drives a DC to DC converter (part 2) which drives a DC motor from 0 V to 20 V .
- Other

6) Requirements: Specify the

- Inputs
- Outputs
- How they relate

7) Analysis: Calculate the values of the components in your circuit to meet the requirements.
8) Simulation: Check your analysis using a circuit simulator, such as PartSim
9) Validation: Build your circuit and verify it meets the requirements
