## ECE 320-Quiz \#2 - Name

Semiconductors, pn Junction, ideal diodes - Fall 2020

1a) Silicon diodes have a 0.7 V drop across them (approximately). What is the cause of this 0.7 V drop?

The potential energy barrier created by the pn junction
The depletion region of the diode. It takes about 0.7V to shrink this down to zero.

1b) Why does the votlage drop across a silicon diode decrease as temperature goes up?

As temperature goes up, you get more and more thermal electrons / holes. This causes the n-type and p-type materials that make up the diode to behave more and more like plain silicon - which would have no voltage drop.
2) The resistance of a thermistor is given by

$$
R=1000 \cdot \exp \left(\frac{3905}{T}-\frac{3905}{278}\right) \Omega
$$

where T is the temerature in degrees Kelvin $(\mathrm{C}+273)$. Find the resistance and the temperature if the voltage at V 1 is 3.50 V for the following circuit.

| Resistance (R) | Temperature (T) |
| :---: | :---: |
| 538.46 Ohms | $290.816 \mathrm{~K}=17.816 \mathrm{C}$ |



$$
V_{1}=\left(\frac{R}{R+1000}\right) 10 V
$$

doing some algebra...

$$
\begin{aligned}
& R=\left(\frac{V_{1}}{10 V-V_{1}}\right) 1000 \\
& R=538.46 \Omega
\end{aligned}
$$

Plugging into the thermistor equation

$$
\begin{aligned}
& 538.46=1000 \cdot \exp \left(\frac{3905}{T}-\frac{3905}{278}\right) \Omega \\
& T=290.816 K \\
& T=17.8 C
\end{aligned}
$$

3) Load Lines: Draw the load line for the following circuit and from the graph, determine Vd and Id

| Load Line | $\mathrm{Vd}($ Vots $)$ | $\mathrm{Id}(\mathrm{mA})$ |
| :---: | :---: | :---: |
| shown on graph | $\mathbf{2 . 1 V}$ | $\mathbf{4 8 m A}$ |


4) The VI characteristics for a diode are

$$
\begin{aligned}
& V_{d}=0.052 \ln \left(10^{8} \cdot I_{d}-1\right) \text { Volts } \\
& I_{d}=10^{-8}\left(\exp \left(\frac{V_{d}}{0.052}\right)-1\right) \mathrm{Amps}
\end{aligned}
$$

Write the voltage node equations for the following diode circuit. (you don't have to solve - just give the equations)
Note: You should end up with six equations:

- Three for the diodes: $\{\mathrm{Id} 1, \mathrm{Id} 2, \mathrm{Id} 3\}$ in terms of $\{\mathrm{V} 1, \mathrm{~V} 2, \mathrm{~V} 3\}$, and
- Three for the voltage nodes


$$
\begin{aligned}
& I_{d 1}=10^{-8}\left(\exp \left(\frac{V_{1}-V_{2}}{0.052}\right)-1\right) \\
& I_{d 2}=10^{-8}\left(\exp \left(\frac{0-V_{2}}{0.052}\right)-1\right) \\
& I_{d 3}=10^{-8}\left(\exp \left(\frac{V_{3}}{0.052}\right)-1\right) \\
& \left(\frac{V_{1}-10}{100}\right)+I_{d 1}+\left(\frac{V_{1}-V_{3}}{500}\right)=0 \\
& -I_{d 1}-I_{d 2}+\left(\frac{V_{2}}{1000}\right)=0 \\
& \left(\frac{V_{3}-V_{1}}{500}\right)+I_{d 3}=0
\end{aligned}
$$

5) Assumd ideal diodes. Determine the voltages and currents assuming ideal silicon diiodes $(\mathrm{Vf}=0.7 \mathrm{~V})$

| V1 | V2 | V3 | Id1 | Id2 | Id3 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 7.8538 V | 7.1538 V | 0.70 V | 7.1538 mA | 0 mA <br> (off) | 14.30 mA |


$V_{3}=0.7$
$V_{2}=V_{1}-0.7$
$\left(\frac{V_{1}-10}{100}\right)+\left(\frac{V_{1}-0.7}{1000}\right)+\left(\frac{V_{1}-0.7}{500}\right)=0$
$V_{1}=7.8538 \mathrm{~V}$
6) Assume ideal siliicon dioes. Determine the voltage, V1, and the currents, I1..I4

| V1 | I1 | I2 | I3 | I4 |
| :---: | :---: | :---: | :---: | :---: |
| 0.70 V | 0 <br> ( off ) | 0 <br> ( off ) | 86.0 mA | 7.0 mA |



Bonus! Where is the error in the following proof that $1=2$ ?

$$
\begin{aligned}
& \text { Assume: } a=b=1 \\
& \qquad \begin{array}{ll}
a b=b^{2} & 1=1 \\
a^{2}-a b=a^{2}-b^{2} & 1-1=1-1 \\
a(a-b)=(a+b)(a-b) & 1(1-1)=(1+1)(1-1) \\
a=a+b & \lll \text { error is here: can't divide both sides by zero } \\
1=2
\end{array}
\end{aligned}
$$

