

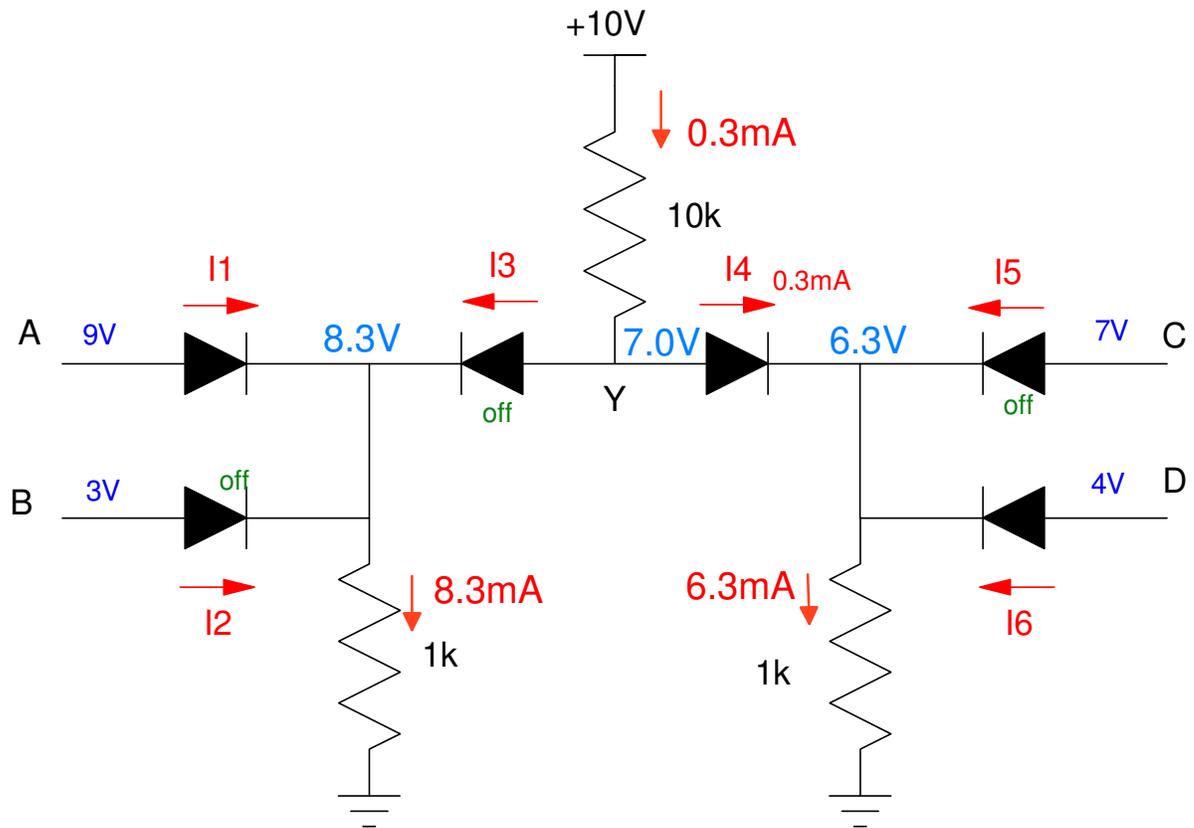
ECE 320 - Quiz #4 - Name _____

Max/Min, Clipper Circuits, Transistors. February 14, 2020

Max/Min Analysis

1) Determine the currents. Assume ideal silicon diodes ($V_f = 0.7V$)

I1	I2	I3	I4	I5	I6
8.3 mA	0	0	0.3mA	6.0mA	0

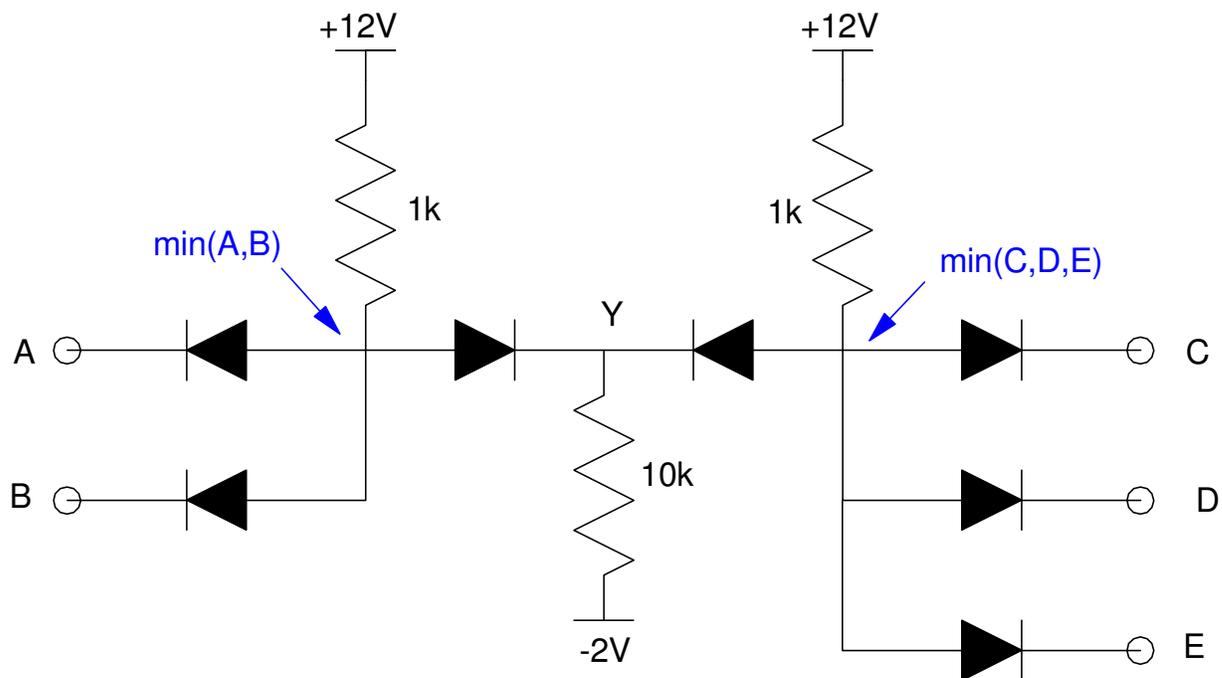


Max / Min Design

2) Design a circuit to implement the function

$$Y = AB + CDE = \max(\min(A, B), \min(C, D, E))$$

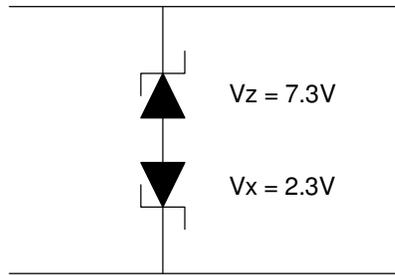
Assume all voltages are in the range of (0V, 10V) analog.



Clipper Circuit: Design

4) Design a circuit to clip the input voltage at +8V and -3V

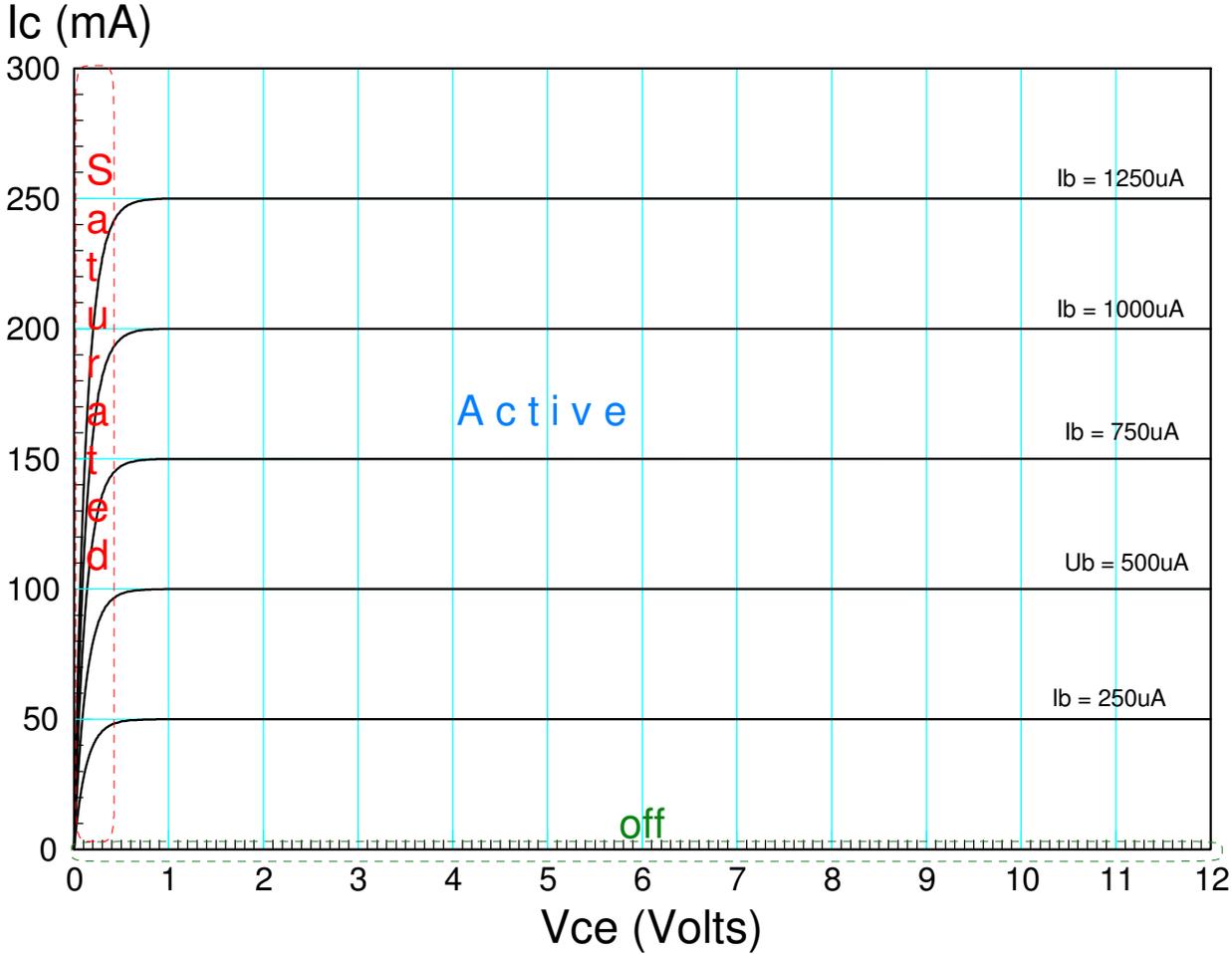
$$Y = \begin{cases} +8V & x > 8V \\ x & -3V < x < 8V \\ -3V & x < -3V \end{cases}$$



Transistor Theory

5) The VI characteristics for an NPN transistor are shown below. Determine the current gain, beta, and label off / active / saturated regions

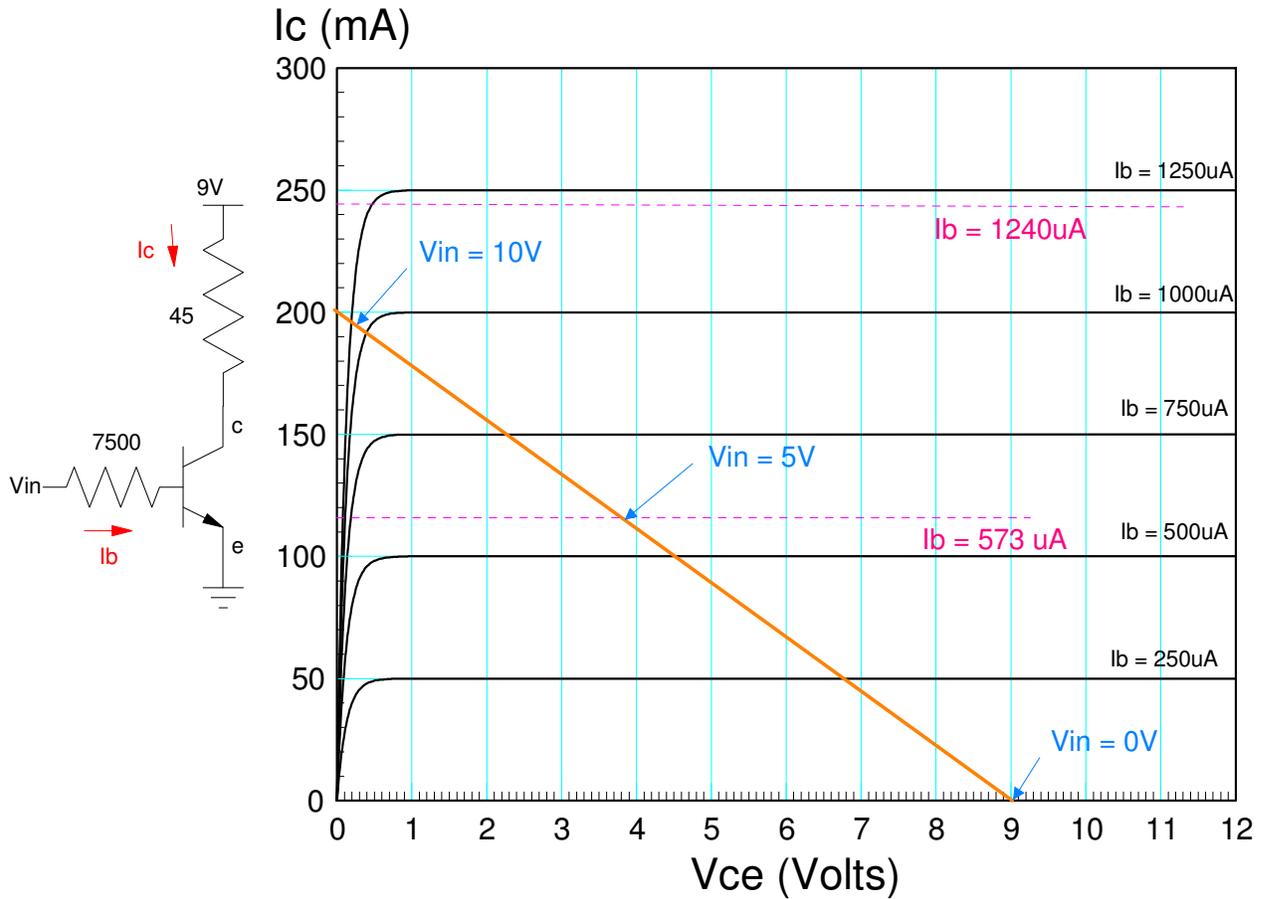
Current Gain (beta)	Off Region	Active Region	Saturated Region
200	show on graph	show on graph	show on graph



Transistor Load Line

6) Draw the load line for the following transistor circuit. Also label the operating points when $X = \{0V, 5V, 10V\}$

Load Line	Operating Point $V_{in} = 0V$	Operating Point $V_{in} = 5.0V$	Operating Point $V_{in} = 10.0V$
show on graph	$V_{ce} = 9.0V$ $I_c = 0mA$	$V_{ce} = 3.84V$ $I_c = 115mA$	$V_{ce} = 0.2V$ $I_c = 196mA$



$$V_{in} = 0V: \quad I_b = 0$$

$$V_{in} = 5V: \quad I_b = \left(\frac{5V - 0.7V}{7500} \right) = 573 \mu A$$

$$V_{in} = 10V: \quad I_b = \left(\frac{10V - 0.7V}{7500} \right) = 1240 \mu A$$

Valentine's Day Bonus: The following functions are for a conchoid, butterfly, another butterfly, and a heart. Which one draws a heart? **answer = a)**

$$a) \quad r = \frac{\sin(t) \sqrt{|\cos(t)|}}{\sin(t) + 1.4} - 2 \sin(t) + 2 \quad b) \quad r = \cos^2(5\theta) + \sin(3\theta) + 0.3 \quad c) \quad r = \exp(\sin(t)) - 2 \cos(4t) - 0.416 \cos(t)$$

$$d) \quad x(t) = 11 \cos(t) - 6 \cos(11t/6) \quad y(t) = 11 \sin(t) - 6 \sin(11t/6)$$

