

ECE 320 - Quiz #7 - Name _____

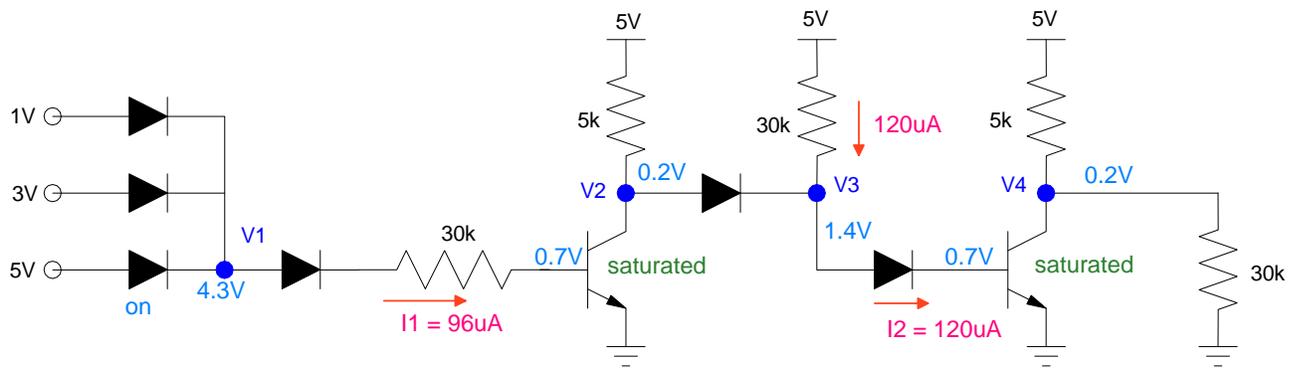
DTL Logic, TTL Logic, MOSFETs. March 12, 2020

1) Determine the voltages for the following DTL gate. Assume ideal silicon diodes ($V_f = 0.7V$) and transistors:

$V_{be} = 0.7V$

- $V_{ce(sat)} = 0.2V$
- $\beta = 100$

| V1 | V2 | V3 | V4 |
|--------------|--------------|--------------|--------------|
| 4.3 V | 0.2 V | 1.4 V | 0.2 V |

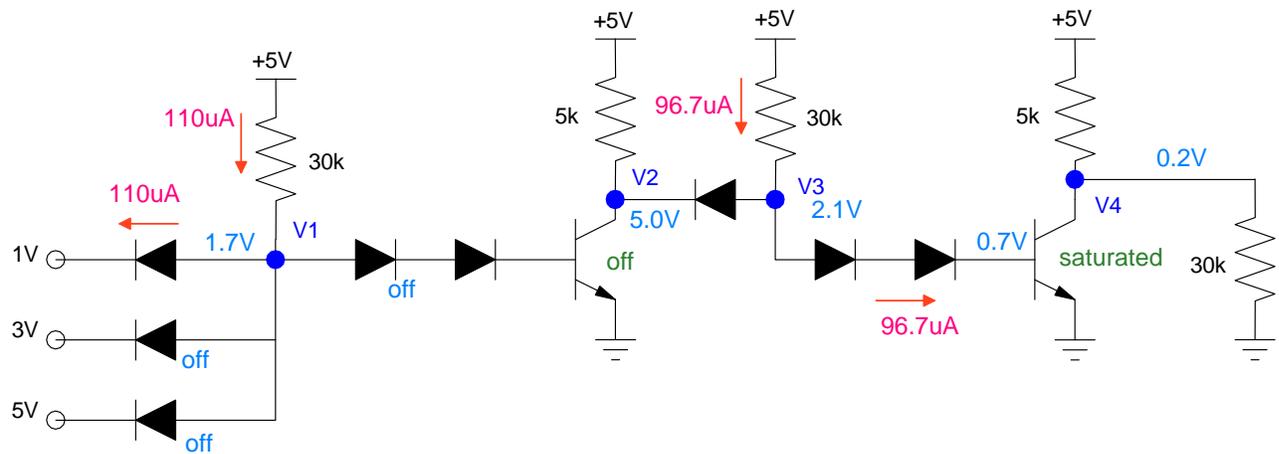


$$I_1 = \left(\frac{5V - 2.1V}{30k} \right) = 96\mu A$$

2) Determine the voltages for the following DTL gate. Assume ideal silicon diodes ($V_f = 0.7V$) and transistors:

- $V_{be} = 0.7V$
- $V_{ce(sat)} = 0.2V$
- $\beta = 100$

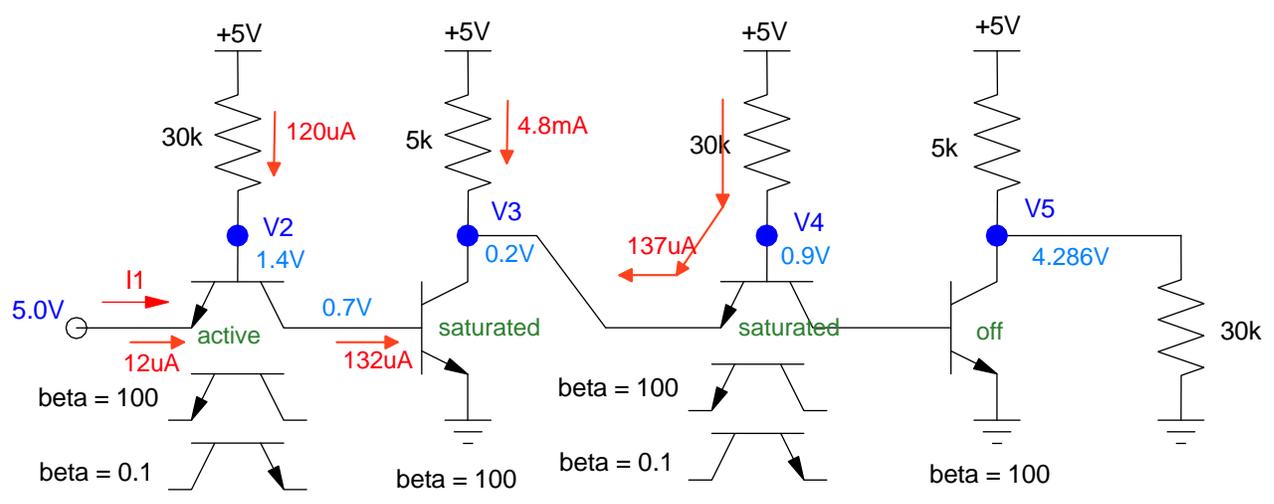
| V1 | V2 | V3 | V4 |
|--------------|--------------|--------------|--------------|
| 1.7 V | 5.0 V | 2.1 V | 0.2 V |



3) Determine the voltages for the following TTL gate. Assume ideal silicon diodes ($V_f = 0.7V$) and transistors:

- $V_{be} = 0.7V$
- $V_{ce(sat)} = 0.2V$
- $\beta = 100$ or 0.1

| I1 | V2 | V3 | V4 | V5 |
|------------------------------|---------------|---------------|---------------|----------------|
| 12 μA | 1.40 V | 0.20 V | 0.90 V | 4.286 V |

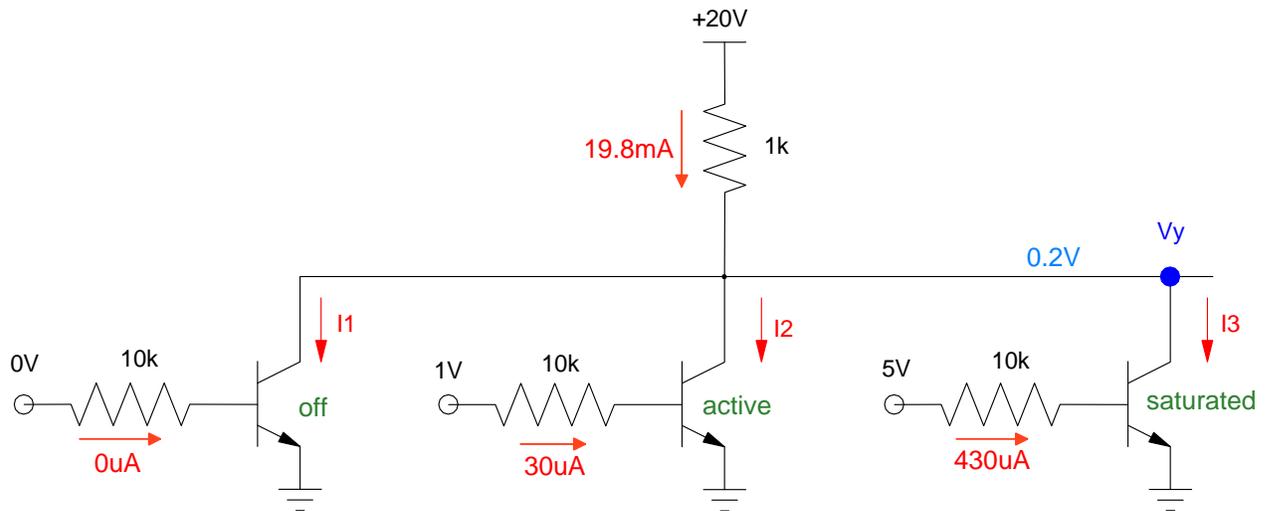


$$V_5 = \left(\frac{30k}{30k+5k} \right) 5V = 4.286V$$

4) Determine the voltages and currents for the following open-collector circuit. Assume ideal silicon diodes ($V_f = 0.7V$) and transistors:

- $V_{be} = 0.7V$
- $V_{ce(sat)} = 0.2V$
- $\beta = 100$

| I_1 | I_2 | I_3 | V_y |
|-------------|---------------|----------------|--------------|
| 0 mA | 3.0 mA | 16.8 mA | 0.2 V |



$$I_2 = \beta I_b = 3.0\text{ mA}$$

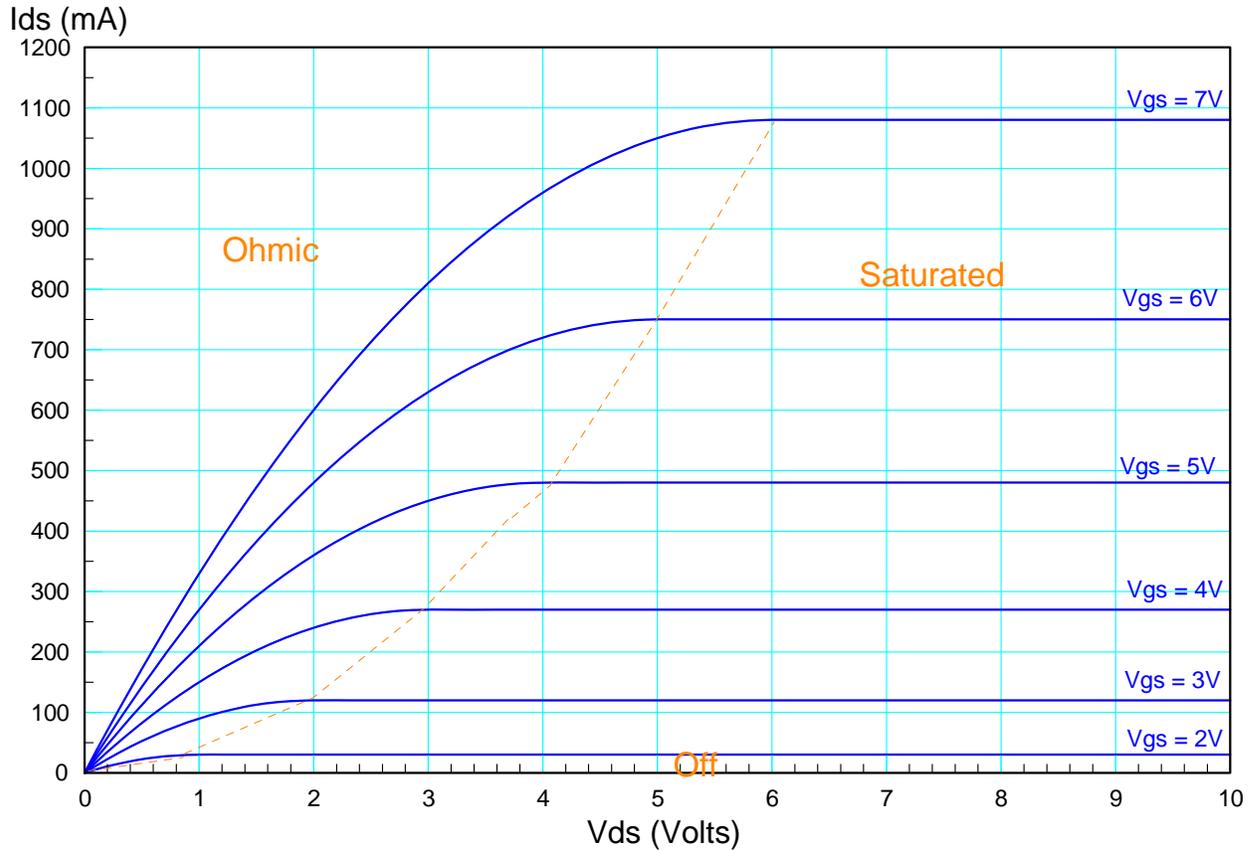
$$I_3 + I_2 = 19.8\text{ mA}$$

$$I_3 = 16.8\text{ mA}$$

5) Determine k_n and label the off / saturated / ohmic regions for the following MOSFET.

| k_n | Off Region | Ohmic Region | Saturated Region |
|-----------------------------|---------------|---------------|------------------|
| 0.06 V/A² | show on graph | show on graph | show on graph |

- Saturated Region: $I_{ds} = \frac{k_n}{2}(V_{gs} - V_{th})^2$ $V_{th} = 1.0V$
- Ohmic Region: $I_{ds} = k_n \left(V_{gs} - V_{th} - \frac{V_{ds}}{2} \right) V_{ds}$



Pick a point: $V_{ds} = 9.0V$, $V_{gs} = 7.0V$, $I_{ds} = 1080mA$

Saturated Region:

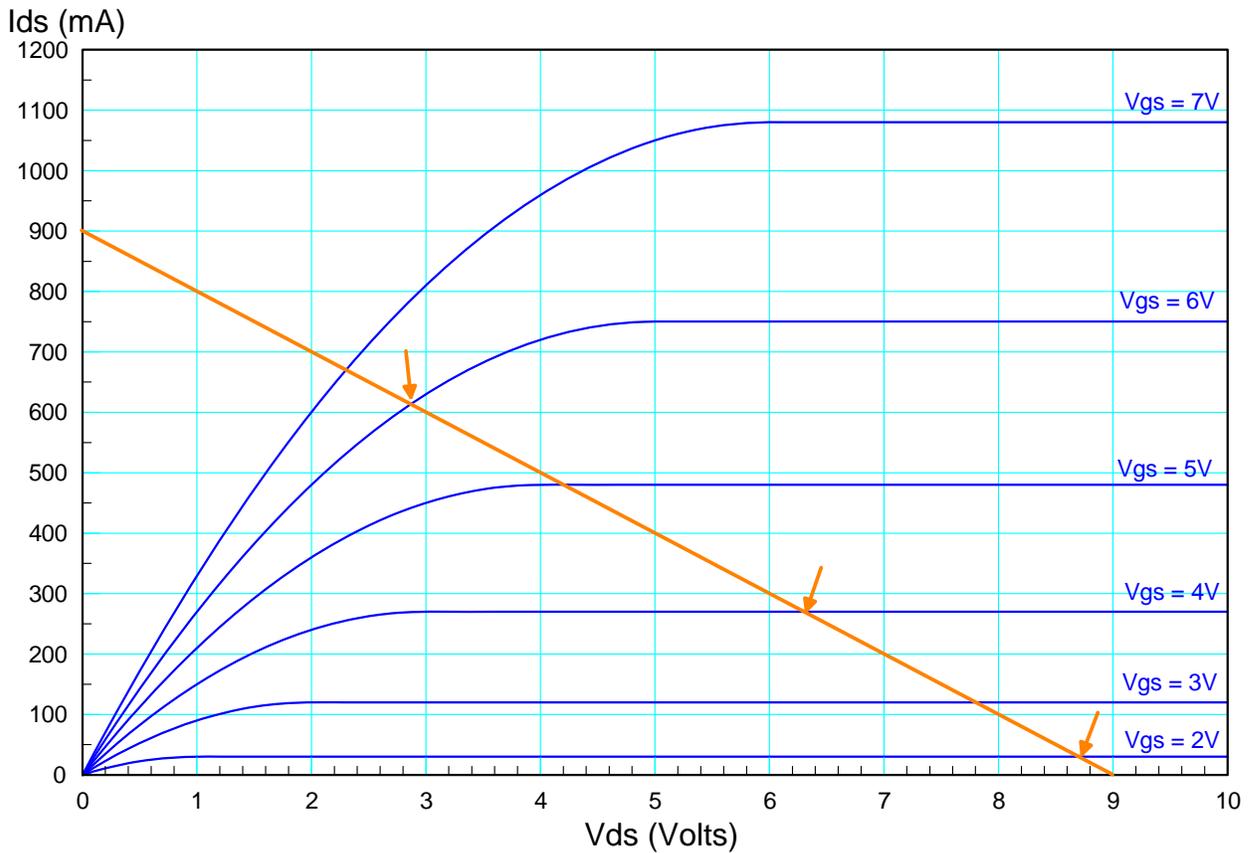
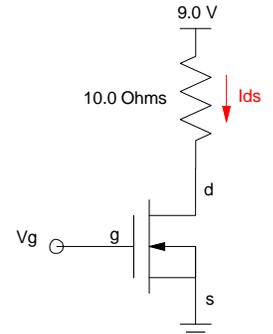
$$I_{ds} = \frac{k_n}{2}(V_{gs} - V_{th})^2$$

$$1.080A = \frac{k_n}{2}(7V - 1V)^2$$

$$k_n = 0.060 \frac{A}{V^2}$$

6) Draw the load-line for the following circuit and determine the q-point (V_{ds} , I_{ds}) for $V_g = \{1V, 3V, 5V\}$

| Load Line | $V_g = 2.0\text{ V}$ | $V_g = 4.0\text{ V}$ | $V_g = 6.0\text{ V}$ |
|---------------|-------------------------|--------------------------|-------------------------|
| show on graph | $V_{ds} = 8.7\text{ V}$ | $V_{ds} = 6.4\text{ V}$ | $V_{ds} = 2.8\text{ V}$ |
| | $I_{ds} = 30\text{mA}$ | $I_{ds} = 260\text{ mA}$ | $I_{ds} = 620\text{mA}$ |



Bernie Sanders Bonus! According to the Las Vegas betting line, which is more likely to happen this year:

- The Vikings winning the Super Bowl: 14:1 odds
- Having a killing frost in June in Fargo (temperature drops below 30F) 156:1 odds (t-score = 2.55)
- Bernie Sanders being elected President: 14:1 odds