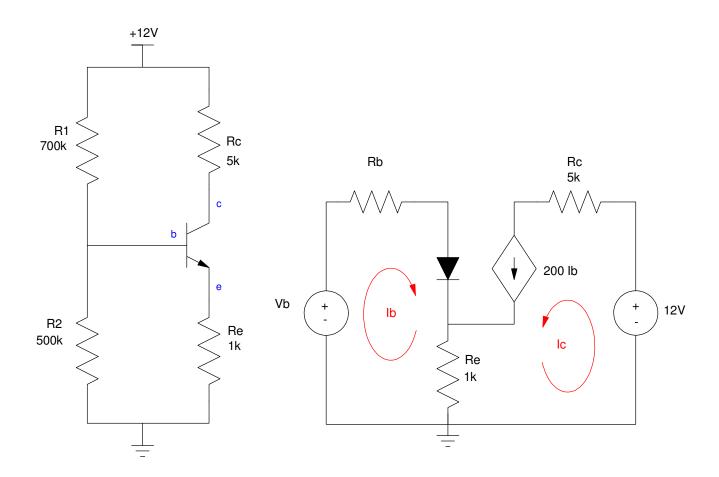
## ECE 321 - Homework #5

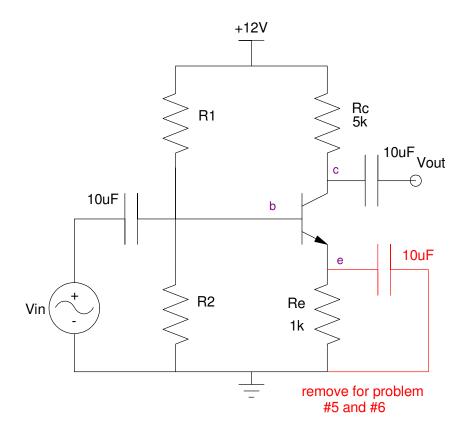
DC Analsis of Transtor Amplfiers, 2-Ports, CE Amplifiers.

- 1) Determine the Q-point for the following transistor circuit. Assume C's are large and assume 3904 transistors:
  - Vbe = 0.7V
  - $\beta = 200$
- 2) Modify this circuit so that
  - The Q-point is stabilized for variations in  $\beta$ , and
  - The Q-point is Vce = 5.0V



Problem #1 and #2

- 3) Draw the small-signal model for the circuit of problem #2 connected as a common emitter amplifier (below). From this, determine the 2-port model
- 4) Simulate this circuit in CircuitLab. Verify each of the 2-port parameters at 1kHz
  - Rin
  - Rout
  - Ao
- 5) Remove Ce. Now draw the small-signal model for the circuit of problem #2. From this, determine the 2-port model for the Common Emitter amplifier
- 6) Simulate this circuit in CircuitLab. Verify each of the 2-port parameters at 1kHz
  - Rin
  - Rout
  - Ao



Problem 3 to 6. Use R1 and R2 from problem #2 (so that Vce = 5V)