

Final Report

ECE 401 - Homework #9

1-on-1 Demo

- Schedule a 1/2 hour time to demonstrate final project
- Requires a functioning PCB

Poster

- 11" x 17" in size
- Group Designator listed
- Project Name Listed
- Use the NDSU Format design
- Project Description/Requirements
- Applications
- Component List or (BOM)
- Circuit lab Schematic
- PCB Schematic
- Top PCB Design (copy image from OshPark)
- Bottom PCB Design (Copy image from OshPark)
- Formula(s)

How to order posters

- Once Posters are completed the files need to be sent to Jeffrey.erickson@ndsu.edu or jacob.glower@ndsu.edu for authorization to print.

Updated Lab Notebook (should include...)

- Previous mini-project assignments
- Bill of materials in Excel format
- Schematic
- Front and back picture of PCB from Oshpark
- Test point signals from CircuitLab / Breadboard / PCB
- { 9V, 5V, ground, Input, Output, Collector }